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** CONTINUING DATA *****

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** FOREIGN APPLICATIONS *****

JAPAN 2002-200055(PAT. 09/07/2002)
 JAPAN 2002-352440(PAT. 12/04/2002)

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35 USC 119 (a-d) conditions met <input checked="" type="checkbox"/> yes <input type="checkbox"/> no <input type="checkbox"/> Met after Allowance				
Verified and Acknowledged Examiner's Signature _____ Initials _____				

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TITLE
 Component-embedded board fabrication method and apparatus for high-precision and easy fabrication of component-embedded board with electronic components embedded in wiring board

FILING FEE RECEIVED 1338	FEES: Authority has been given in Paper No. _____ to charge/credit DEPOSIT ACCOUNT No. _____ for following:	<input type="checkbox"/> All Fees <input type="checkbox"/> 1.16 Fees (Filing) <input type="checkbox"/> 1.17 Fees (Processing Ext. of time) <input type="checkbox"/> 1.18 Fees (Issue) <input type="checkbox"/> Other _____ <input type="checkbox"/> Credit
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